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				Application Number	10/773,450-Conf. #3611
				Filing Date	December 2, 2004
				First Named Inventor	Chen-Chih Huang
				Art Unit	2816
				Examiner Name	L. M. Nguyen
Sheet	1	of	1	Attorney Docket Number	SUND-501CIP

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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LM	CA	HERVÉ MARIE AND PHILIPPE BELIN, R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display, IEEE JOURNAL OF SOLID-STATE CIRCUITS, JULY 1998, pages 1009-1013, VOL. 33, NO. 7, PHILIPS SEMICONDUCTORS, CAEN, FRANCE			

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